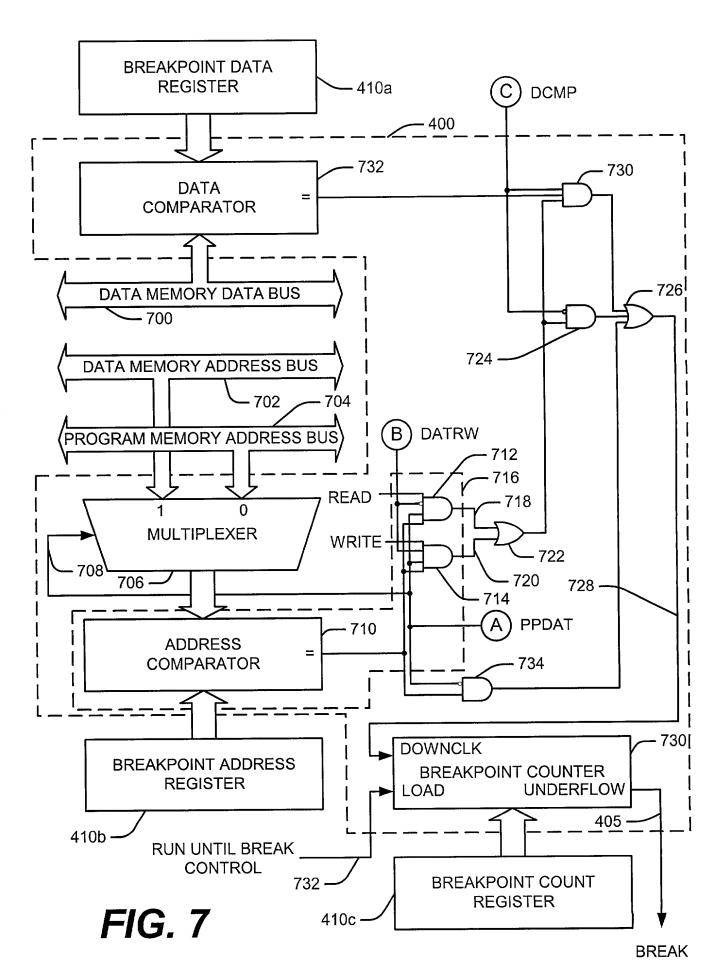


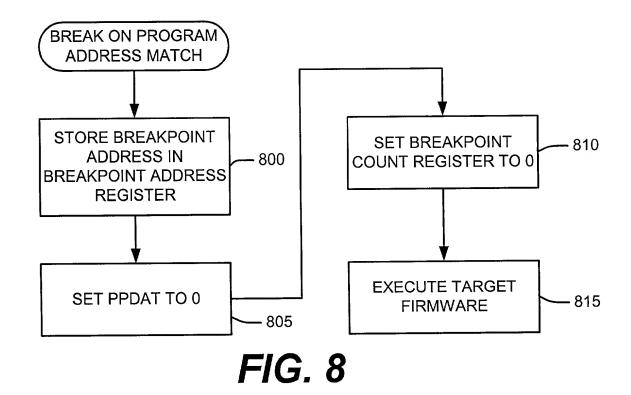
FIG. 5

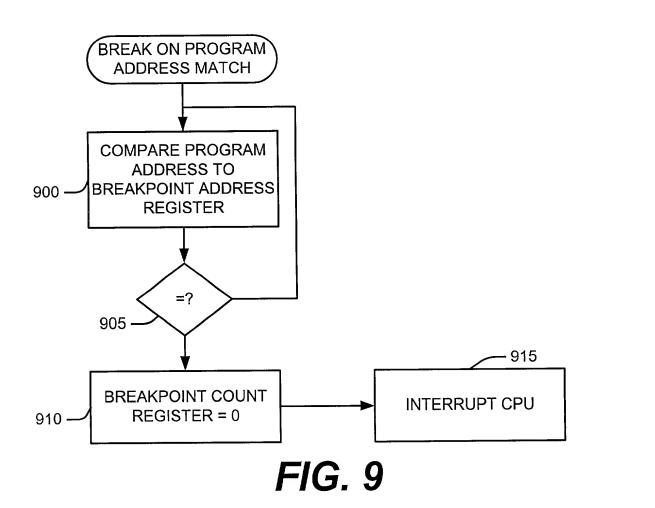
## **DEBUG REGISTER**

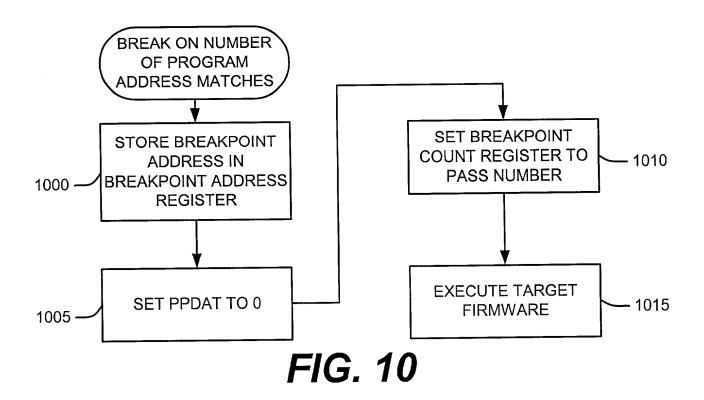
DEBUG REGISTER	
INBUG FREEZ SSTEP SHDW BRB7 BRB6 BTRIS7 BTRIS6	^
600	J
ICKBUG REGISTER	
PPDAT DATRW DCMP Stkbrk BKA19 BKA18 BKA17 BKA16 60	5
	5
(A) (B) (C) BITS 16-19 OF BREAKPOINT ADDRESS REGISTER	
BIGBUG REGISTER	
BKA15 BKA14 BKA13 BKA12 BKA11 BKA10 BKA09 BKA08	^
61	U
BITS 15-8 OF BREAKPOINT ADDRESS REGISTER	
GLOBUG REGISTER	
BKA07 BKA06 BKA05 BKA04 BKA03 BKA02 BKA01 BKA00 61	5
	J
BITS 7-0 OF BREAKPOINT ADDRESS REGISTER BITS 7-0 OF BREAKPOINT DATA REGISTER	
CNTBUG REGISTER	
BKC07         BKC06         BKC05         BKC04         BKC03         BKC02         BKC01         BKC00	<b>)</b> (
	.0
BITS 7-0 OF BREAKPOINT COUNT REGISTER	
OL OBLIG RECIOTER	
SLGBUG REGISTER	
RSBUG	

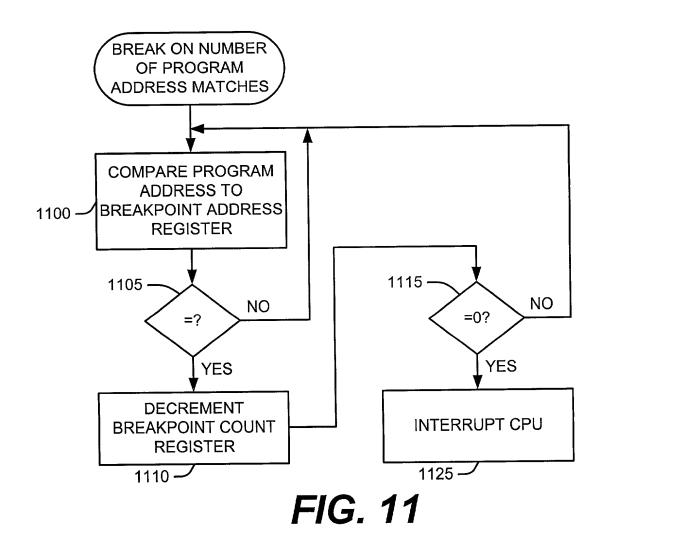
## FIG. 6











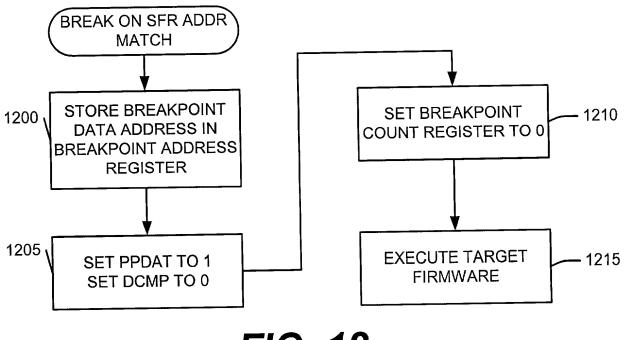


FIG. 12

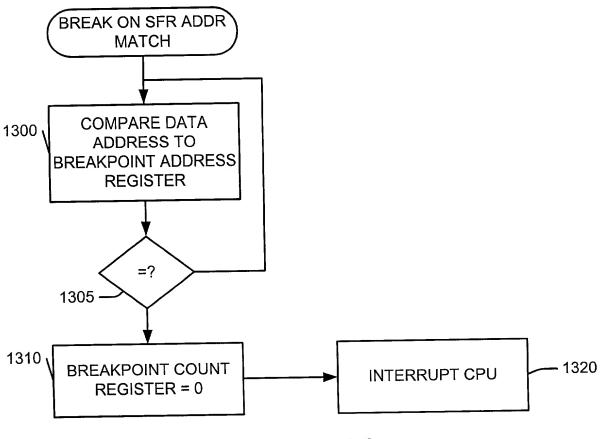


FIG. 13

